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Appl. No. 09/841,582
Amdt. Dated January 29, 2007
Reply to Office Action of November 30, 2006

REMARKS***Rejections Under 35 U.S.C. 103(a)***

The Examiner has rejected claims 6-9 under 35 U.S.C. 103(a) as being unpatentable over United States Patent No. 5,953,588 to Camien in view of the publication to Wolf titled *Silicon Processing for the VLSI Era*, Process Integration; Volume 2; 1990, pp 334, 335, & 337. Claims 6-9 were also rejected as being unpatentable over United States Patent No. 5,879,964 to Paik in view of Wolf.

Applicant has amended claim 6 to include the limitation that the surfaces of the semiconductor chips at which the electrodes are formed are releasably attached to an adhesive sheet. None of the references cited by the Examiner anticipate or render obvious the invention of amended claim 6. Applicant respectfully request consideration and allowance of amended claim 6.

Claims 7-9 depend from claim 6. Applicant asserts that because claim 6 is allowable, claims 7-9 are also allowable. Applicant respectfully requests reconsideration and allowance of claims 7-9.

Newly Added Claims

Applicant in this amendment has added claim 10 which depends from claim 6. Applicant asserts that because claim 6 is allowable, claim 10 is also allowable. Applicant respectfully requests consideration and allowance of claim 10.

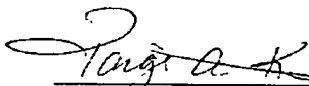
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In light of the foregoing, Applicants respectfully submit that the prior art fails to teach or suggest Applicants invention is now specified. Accordingly, Applicants submit that all claims now standing condition for allowance.

Applicant hereby authorizes the Commissioner to charge deposit account number 50-3891 for any fees due in connection with this filing. In

Respectfully submitted,

Date: 1/29/07



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